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			Application / Conf. N	lo.	10/783,216 / 6678	
T	RANSMITTAL		Filing Date		February 20, 2004	
	FORM		First Named Inventor		Tim Tuan	
(to be used fo	or all correspondence after initial fil	ling)	Examiner Name		Unknown	
Mail Stop:			Art Unit		2816	
Express Mail Receipt No.			Patent No.			
	es in This Submission		Attorney Docket Nui	mber	X-1462-1P US	
		ENCLOSUR	IES (check all that app	ply)		
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Express Aba	andonment Request		nvert a ional Application		Other Enclosure(s) (please identify below):	
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Response to incomplete	o Missing Parts/ Application	Remarks				
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Firm <i>or</i> Customer Number Attn:	Justin Liu		24309 (Customer Number)		Reg. Number 51,959	
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Date	July 30, 2004		Charge any add to our Deposit A	ditional fe Account I	ees required/credit any overpayment Number: 24-0040	
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IN THE UNITED STATES PATENT OFFICE

Tim Tuan et al.

Assignee:

Xilinx, Inc.

Title:

"Tuning Programmable Logic Devices for Low-Power

Design Implementation"

Serial No.:

10/783,216

File Date: 02/20/2004

Examiner:

Unknown

Art Unit:

2816

Docket No.: X-1462-1P US Conf. No.:

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449A/PT. One copy each of the four non-patent references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu

Attorney for Applicants

Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450/, øn July 30, 2004.

Julie Matthews

Name

JL: jam

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		Application / Conf. No.	10/783,216 / 6678				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			CLOSURE	Filing Date	February 20, 2004 Tim Tuan		
				First Named Inventor			
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(use as many sheets as necessary)		Examiner Name	Unknown				
Sheet	1	of	2	Attorney Docket Number	X-1462-1P US		

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
muais	110.	US- 5,671,149	09-23-97	Brown	Figures Appear
alember - alember - committee () <u></u>	US- 6,172,518 B1	01-09-01	Jenkins, IV. et al.	
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		FORE	IGN PATENT DOC	UMENTS		
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Initials *	No.¹	Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	or Relevant Figures Appear	T 6
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¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁴ Applicant is to place a check mark here if English language Translation is attached.

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Attorney Docket Number

OTHER - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No ^{.1}	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Τ2
		Jae Y. Park, Mark G. Allen; "A Comparison of Micromachined Inductors with Different Magnetic Core Materials"; 1996 Electronic Components and Technology Conference; 1996 IEEE; pp. 375-381.	
		Thomas D. Burd et al.; "A Dynamic Voltage Scaled Microprocessor System"; 2000 IEEE; IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000; pp. 1571-1580.	
		Anthony J. Stratakos et al.; "A Low-Voltage CMOS DC-DC Converter for a Portable Battery-Operated System"; 1994 IEEE; pp. 619-626.	
		Anthony John Stratakos; "High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications," Chapter 3; "DC-DC Converter Fundamentals"; pp. 42-78.	
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